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APPLICANT Hideto HIDAKA	
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Documt Number Number-Kind Code2 (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AP	A1	US 2002/0172073	11/21/2002	HIDAKA	
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AP		JP 2002-170375	06/14/2002	HIRAI		abstract	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
AP		SCHEUERLEIN, et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch In Each Cell" IEEE International Solid-State Circuits Conference (2000) TA 7.2	
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EXAMINER	DATE CONSIDERED 8/17/04

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